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71 Applicant: HITACHI, LTD., 6, Kanda Surugadai 4-chome
 Chiyoda-ku, Tokyo 100 (JP)
 Applicant: Hitachi Seibu Soft Ware Company, Ltd., 4-6,
 Kitahama Higashi-ku, Osaka (JP)

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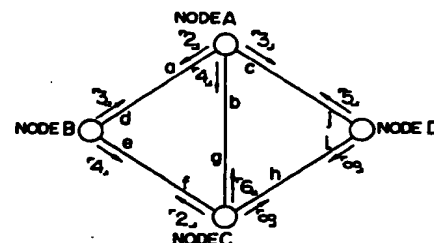
72 Inventor: Suzuki, Michio, 559-1-28-103, Kamoshidacho
 Midori-ku, Yokohama (JP)
 Inventor: Kanoh, Takashi, 9-18, Susukino-3-chome
 Midori-ku, Yokohama (JP)
 Inventor: Nozaki, Tetsu, 412-1, Kawakamicho, Totsuka-ku
 Yokohama (JP)
 Inventor: Kashi, Jiro, 18-1, Nijigaoka-1-chome Asoo-ku,
 Kawasaki-shi (JP)
 Inventor: Takasugi, Yasushi, 6-11, Numashiroshicho,
 Hadano-shi (JP)

25 Designated Contracting States: DE FR GB

73 Representative: Strehl, Schubert-Hopf, Grosseing, Schutz,
 Widemeyerstrasse 17 Postfach 22 03 45,
 D-8000 München 22 (DE)

54 Routing control method in a packet switching network.

57 In a communication network, in which a plurality of switching nodes (A, B, C, D) are connected with each other through a plurality of relay lines and one (A) of the switching nodes acts as a center node for network routing, the center node (A) estimates the state of delay of data for every relay line on the basis of information previously inputted, indicating the communication network configuration, and the state of delay of data reported by each of the switching nodes, and informs each of the switching nodes (B, C, D) of said estimated state of delay of data. Each of the switching nodes (B, C, D) decides the relay line giving the shortest delay of data for every destination, to which it outputs data, on the basis of the state of delay at the relevant switching node itself and that received from the center node (A).



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ROUTING CONTROL METHOD IN A PACKET
SWITCHING NETWORK

1 BACKGROUND OF THE INVENTION

FIELD OF THE INVENTION

This invention relates to a routing control method in a data switching network, and more specifically to an adaptive routing method permitting to take measures against occurrence of load fluctuations or obstructions.

DESCRIPTION OF PRIOR ART

Recently a data switching network, more specifically a packet switching network is adopted in many data communication networks such as VAN (value-added network) and so forth. In a packet switching network, unless each switching node has any information about the circumstances of the network, it is obliged to send messages (packets) through each of routes at random, because it cannot select the optimum route to the destination station, and naturally this gives rise to a problem such as delay in transmission of information, etc. Consequently adaptive routing techniques permitting to select the optimum route, depending on the circumstances of the network become important to realize a packet switching network in practice.

Concerning the adaptive routing method, heretofore, various studies have been reported e.g. in Routing Techniques for Store-and-Forward Computer-

1 Communication Networks, ICC, 1971 by G.L. Flutz and
L. Kleinrock. However, according to the prior art
adaptive routing method, since updating of the estimated
delay at the center has been effected only when some
5 obstructions have occurred or when the network configuration
has been modified and since the estimated delay
has not depended on the load of each of the switching
nodes but has been fixed in the cases other than those
described above, selection of the route has been
10 effected, taking into account the load at the switching
node determining the route, but without taking into
account the load at relay switching nodes receiving
messages.

SUMMARY OF THE INVENTION

15 The object of this invention is to provide an
adaptive routing control method, by which each of the
switching nodes can send and relay data, while selecting
a route enabling to make the delay time to the destination
node of the data shortest.

20 In order to achieve this object, the routing
method according to this invention for a communication
network, in which a plurality of switching nodes are
connected with each other through a plurality of relay
lines and one of the switching nodes acts as a center
25 node for network routing, is characterized by comprising:
a step, by which each of the switching nodes
reports the state of delay of data for each of relay

1 lines connected thereto to the center node;

a step, by which the center node estimates the state of delay of data for every relay line which the relevant switching node can select, when it sends

5 data to one of the other switching nodes on the network, on the basis of information previously inputted, indicating the communication network configuration, and the state of delay of data reported by each of the switching nodes;

10 a step, by which the center node informs each of the switching nodes of the estimated state of delay of data for the relay line which the relevant switching node can select; and

a step, by which each of the switching nodes
15 decides the correspondence relation between the other switching nodes on the network and the relay line to be selected, on the basis of the state of delay of data received from the center node and the real state of delay of data at the relevant switching node, whereby
20 each of the switching nodes outputs data to one of the relay lines according to the correspondence relation.

More specifically, according to this invention, the state of delay of data for each of the relay lines estimated by the center node is represented e.g. by the
25 duration of a delay of data from the switching node adjacent to the relevant switching node, which sends the data, on each of the relay lines to the destination switching node, to which the data are directed, and

1 the relevant switching node decides the relay line, for
which the delay of data to the destination switching
node is the shortest, on the basis of the sum of the
delay of data received from the center node for each
5 of the relay lines and the real delay of data at the
relevant switching node for each of the relay lines.

According to a modification of this invention,
the state of delay of data for each of the relay lines
estimated by the center node is a numerical value
10 increased or decreased depending on the state of delay
of data reported by each of the switching nodes, using
the number of relay lines from the switching node
adjacent to the relevant switching node, which sends
the data, on each of the relay circuits to the destina-
15 tion switching node, to which the data are directed, as
the initial value; and the relevant switching node
decides the relay line, for which the delay of data to
the destination switching node is the shortest, on the
basis of the sum of the numerical value representing
20 the delay of data received from the center node for each
of the relay lines and the numerical value representing
the real delay of data at the relevant switching node
for each of the relay lines.

The foregoing and other objects, advantages,
25 manner of operation and novel features of the present
invention will be understood from the following detailed
description when read in connection with the accompanying
drawings.

1 BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a scheme illustrating the system structure of a packet switching network;

5 Fig. 2 is a block diagram illustrating the structure of a switching node;

Fig. 3 is a scheme for explaining a model of the packet switching network;

Fig. 4 is a connection state table used in the host switching node according to this invention;

10 Fig. 5 is a scheme for explaining the state of lines in the network;

Figs. 6A to 6D show information of switching nodes A to D, respectively, reported to the center node;

15 Fig. 7 indicates an estimated delay table 21;

Fig. 8 represents schemes for explaining the line selection at the node C;

Fig. 9 is a table 41 indicating the distance between adjacent nodes used in another embodiment; and

20 Fig. 10 shows another embodiment of the estimated delay table.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

Hereinbelow some embodiments of this invention will be explained, referring to the drawings.

25 (Embodiment 1)

Fig. 1 is a scheme illustrating the system structure of a packet switching network. Packet

1 switching nodes 1 to 4 are connected with each other
through relay lines, each of which nodes can be con-
nected to a multiplexer, to packet type terminals 5, 6
through a CCITT recommendation X.25 interface, or to
5 another packet switching network 8, etc. through a
CCITT recommendation X.75 interface. Fig. 2 is a block
diagram illustrating the general structure of hardwares
of the packet switching nodes 1 to 4, in which a processor
17, a memory 16, and line interface adapters 19 (19-1
10 to 19-4) are connected with each other through a bus 18.
Usually a packet switching node receives a plurality of
subscriber lines and a plurality of relay lines and
packets received through these lines are once stored in
the memory 16 via the line interface adapters 19. After
15 that the processor 17 selects a sending line for every
packet according to its destination and sends it
through related one of the line interface adapters 19-1
to 19-4. The processor 17 has some routing table
(usually a correspondence table between destination
20 switching nodes and sending lines) in the memory 16 and
this selection of the sending line is effected by
referring to the routing table.

Fig. 3 is a modelled scheme showing switching
nodes and relay lines in Fig. 1. Here it is supposed
25 that the node A has the function of the network routing
center. The notations a --- j are line numbers for
identifying the sending lines from each of the switching
nodes.

1 Fig. 4 shows a connection state table 20,
which the node A having the function of the center holds
as network configuration information. The connection
state table 20 shows with which nodes and through which
5 lines each of the switching nodes is connected and is
updated, when the network configuration is changed.
Fig. 5 shows an example of states of the queue length
for each of the lines at each of the nodes A to D.
Figs. 6A to 6D show report content 30A-30D on the state
10 of lines sent by each of the nodes A, B, C and D to the
center. Each of the nodes estimates the delay time to
the node adjacent thereto on the basis of the queue
length produced at each of the lines of the node itself
and the estimated delay time is reported periodically
15 to the center in the form of a packet. This delay time
can be the queue length itself. In the case where
obstructions are produced in the network, the estimated
delay time for a line for which it is impossible to
send any packet due to an obstruction is infinite.

20 Fig. 7 is a scheme illustrating the estimated
delay time table 21 held by the network routing center
(node A). The node A having the center function forms
an estimated delay time table 21 periodically or every
time it receives a report from each of the nodes,
25 on the basis of the connection state table 20 and
reports 30A-30D from each of the switching nodes
indicated in Figs. 6A-6D. The content of the estimated
delay time table 21 indicates the minimum value of

1 the estimated delay time from the node adjacent to the
relevant node to the destination node, in the case where
one of the nodes selects a certain sending line. The
lines on the estimated delay time table correspond to
5 the lines at each of the switching nodes in Fig. 3 and
the state of the queue length of each of the line
corresponds to the numerical values indicating the delay
time in Fig. 5. The lines h and i in Fig. 5, whose nu-
merical value is indicated by " ∞ ", are broken down, or the
10 notation means that their queue length is infinite (in
practice it exceeds a certain threshold value). Fig.
7 indicates estimated delay time from the adjacent node
to the destination node. For example, the estimated
delay time for the line e at the source node B indicates
15 the minimum value of the sum of those in lines from the
adjacent node C on the line e. That is, taking the
route from the node C to the node A as an example, for
the route $C \rightarrow B \rightarrow A$ it is $2 + 3$, i.e. 5, for the route
 $C \rightarrow A$ it is 6 and for the route $C \rightarrow D \rightarrow A$ it is ∞ .
20 Among these values the smallest value "5" is the value
indicating the estimated delay time from the node B to
the node A via the line e. Further, for the route from
the node C to the destination node A via the route h
indicated in Fig. 7, since the value in question is the
25 delay time from the adjacent node D on the route h
indicated in Fig. 7 to the node A, it is not ∞ but 5.

The node A having the center function send
the relevant part of the estimated delay time table 21

1 in the form of a packet as a bias table to each of the
nodes indicated as source nodes indicated in the
estimated delay time table 21. For example, for the
node C the node A sends the part 31 enclosed by a broken
5 line in Fig. 7 as the bias table.

When the node C receives a message, whose
content is the bias table 31 stated above through the
relay line g, it takes-in these data through a line
interface adapter 19 and writes them in a predetermined
10 area on a memory 16 by means of a processor 17.

The processor 17 prepares a waiting queue
table 32 indicated in Fig. 8 for its own node at that
moment on the basis of send-out waiting queues Qf, Qg
and Qh, which are produced in the line adapter of its
15 own node, and adds it to the value of the bias table 31.
A table 33 is obtained by this addition operation and
in this way it becomes possible to prepare a sending
line table 34 for each of the destination nodes by
selecting the line for which the delay time is shortest.

20 When the relevant node receives a packet to be relayed
from each of the lines, it stores the packet once in
the memory 16, refers to the sending line table 34
according to its destination node and sends the packet
to the corresponding line interface adapter.

25 The line interface adapters 19-1 to 19-4
inform periodically the processor 17 of the state
(presence or absence of obstructions, queue length) of
the lines of its own adapter. The processor 17 brings

1 the state of all the adapters and reports it to the
center as a state message 30C.

The nodes A, B and D performs the same processing as that stated above for the node C. In this way,

5 for each of the packets, which have been produced in
the network, a routing to its destination node is
effected, while selecting lines giving the shortest
delay time from the relevant node to the destination
node.

10 In the embodiment described above, although a
case, where reports of state information 30 (30A-30D)
from each of the packet switching nodes to the center
is effected periodically has been explained, the reports
must not be effected necessarily periodically, but they
15 can be reported, only when some variations are produced,
such as in the case where an obstruction is produced or
removed, concerning the line state; in the case where
the value exceeds a threshold value determined appropriately
for each of the lines, concerning the state of
20 the queue length; in the case where the value decreases
from the state, where it is over the threshold value,
to the state, where it is thereunder; etc. An embodiment
for this case will be explained below. In this second
embodiment also the network configuration indicated in
25 Fig. 3 is presumed similarly to the embodiment 1.

(Embodiment 2)

The center has an internodal connection
distance table 41 indicated in Fig. 9, which represents

1 with which switching nodes each of them is connected.
The center obtains the number of hops (number of routes)
from the node adjacent to the relevant node to the
destination node, which is distributed as an estimated
5 delay time table replacing the bias table stated above
to each of the switching nodes. Fig. 10 shows an
example of estimated delay time tables 42 for the node
B. When it is distributed, it is also conceivable that
each of the switching nodes has no estimated delay time
10 table for its own node. In this case it may be trans-
mitted thereto by broadcast.

Each of the general switching nodes selects
the sending line for which the sum of the estimated
delay time stated above from the adjacent node to the
15 destination node and the queue length for each of the
sending lines of the own node is the smallest. In this
case, in order to normalize various line speeds, it is
preferable to use a value obtained by dividing the real
queue length by a threshold queue length appropriately
20 determined according to the line speed as a value
representing the queue length.

Each of the switching nodes reports, to the
center, obstructions in lines, that the queue length
for the sending line of its own node exceeds a certain
25 threshold queue length, removal of the obstructions in
the lines, or that the value decreases from the state,
where it is over the threshold value, to the state,
where it is thereunder.

1 When some obstructions are produced in lines,
the center changes the relevant part in the internodal
connection distance table 41 indicated in Fig. 9 to a
non-connection state (1 + 0). In this connection state
5 the center node prepares again an estimated delay time
table by using the number of hops from the adjacent
switching node to the destination node for each of the
switching nodes and distributes it to them. Further,
concerning the reports in the case where the queue
10 length exceeds the threshold value, the center node
changes the content of the relevant line portion in the
internodal connection distance table 41 from 1 to 2,
prepares a new estimated delay time table similarly to
the case of obstructions in lines, and distributes it
15 to each of the switching nodes.

Each of the switching nodes holds these tables
in the memory 16 and when the processor 17 transmits a
packet received from another switching node, it sends
the packet in the line, for which the sum of the
20 estimated delay time and the queue length of its own
line is the shortest, referring to the tables, similarly
to the embodiment 1.

Further, in the embodiment described above,
although the case where the estimated delay time is
25 obtained on the basis of the waiting queue length
produced in each of the line or the case where the
estimated delay time is calculated on the basis of the
relay stages has been described, it is possible to

- 1 calculate the estimated delay time by using either other
information indicating the load state, such as the use
rate of line, the number of bits of real data in a
waiting queue packet, the use rate of the processing
5 device, etc.

Furthermore, although an example of the one-processor structure has been shown as the structure of the packet switching nodes, this invention can be applied to the case, where the structure of hardwares
10 of a switching node includes a plurality of processors and this invention is applicable independly of the structure of hardwares. Still further the state of the queue length may be caught not through the report from the line interface adapter to the processor, but
15 directly by the processor itself.

As clearly shown by the two embodiments described above, according to this invention, since the estimated delay time table in each of the switching nodes is appropriately modified depending on the state
20 of the lines in the packet switching network or the state of the queue length, even when looping phenomena, by which a packet circulates through more than three packet switching nodes, or ping-pong phenomena, by which a packet goes to and from two adjacent switching
25 nodes, are produced, once a new estimated delay time table corresponding to the real state has been distributed, these phenomena can be eliminated. In addition, since the route giving the shortest estimated delay

1 time is always selected, depending on the state of the
packet switching network, an effect to reduce the mean
transmission delay time of a packet in an network can
be obtained with respect to the system, in which no
5 routing table is modified depending on the state of the
network.

As explained above, according to this inven-
tion, since the sending line is determined on the basis
of the total delay time to the destination node calcu-
10 lated by using an estimated delay time table prepared
by the routing control center, referring to the load
state reported by each of the switching nodes, and the
waiting queue of the relevant node, it is possible to
select a route, depending not only on obstructions in
15 lines or switching nodes but also on the load state at
receiving switching nodes.

CLAIMS:

1. A routing method for a communication network, in which a plurality of switching nodes (A, B, C, D) are connected with each other through a plurality of relay lines and one (A) of said switching nodes acts as a center node for network routing, comprising:

a step of each of said switching nodes (B, C, D) reporting the state of delay of data for each of relay lines connected thereto to said center node (A);

a step of said center node (A) estimating the state of delay of data for every relay line which the relevant switching node (B, C, D) can select, when it sends data to one of the other switching nodes on the network, on the basis of information previously inputted, indicating the communication network configuration, and the state of delay of data reported by each of said switching nodes;

a step of said center node (A) informing each of said switching nodes (B, C, D) of said estimated state of delay of data for the relay line which the relevant switching node can select; and

a step of each of said switching nodes (B, C, D) deciding the correspondence relation between the other switching nodes on the network and the relay line to be selected, on the basis of the state of delay of data received from said center node (A) and the real state of delay of data at said relevant switching node,

whereby each of said switching nodes (B, C, D) outputs data to one of said relay lines according to said correspondence relation.

2. A routing method according to Claim 1, in which the state of delay of data for each of said relay lines estimated by said center node (A) is represented by the duration of a delay of data from the switching node adjacent to the relevant switching node, which sends the data, on each of the relay lines to the destination switching node, to which said data are directed, and said relevant switching node decides the relay line, for which the delay of data to said destination switching node is the shortest, on the basis of the sum of the delay of data received from said center node (A) for each of the relay lines and the real delay of data at said relevant switching node for each of the relay lines.

3. A routing method according to Claim 1, in which the state of delay of data for each of said relay lines estimated by said center node (A) is a numerical value increased or decreased depending on the state of delay of data reported by each of said switching nodes (B, C, D) using the number of relay lines from the switching node adjacent to the relevant switching node, which sends the data, on each of the relay lines to

the destination switching node, to which said data are directed, as the initial value; and

said relevant switching node decides the relay line, for which the delay of data to said destination switching node is the shortest, on the basis of the sum of the numerical value representing the delay of data received from said center node for each of the relay lines and the numerical value representing the real delay of data at said relevant switching node for each of the relay lines.

4. A routing method according to any of claims 1 to 3, in which each of said switching nodes (B, C, D) informs periodically said center node (A) of the state of delay of data.

5. A routing method according to any of claims 1 to 3, in which each of said switching nodes (B, C, D) informs periodically said center node (A) of the state of delay of data for every relay line, when a variation exceeding a predetermined value is produced in the state of delay of data in either one of relay lines connected to the relevant switching nodes.

6. A routing method according to any of claims 1 to 3, in which each of said switching nodes (B, C, D) sends data in the form of a packet and said state of delay of data is represented by queue lengths of packets for each of said relay lines.

F I G. 1

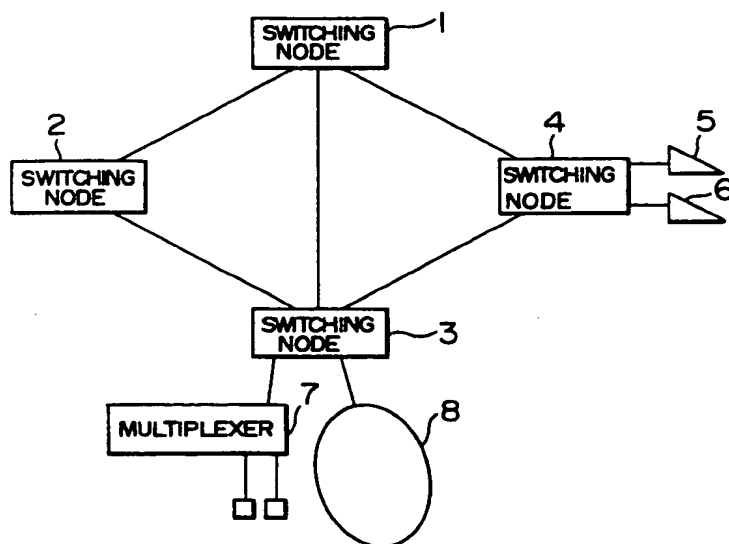


FIG. 2

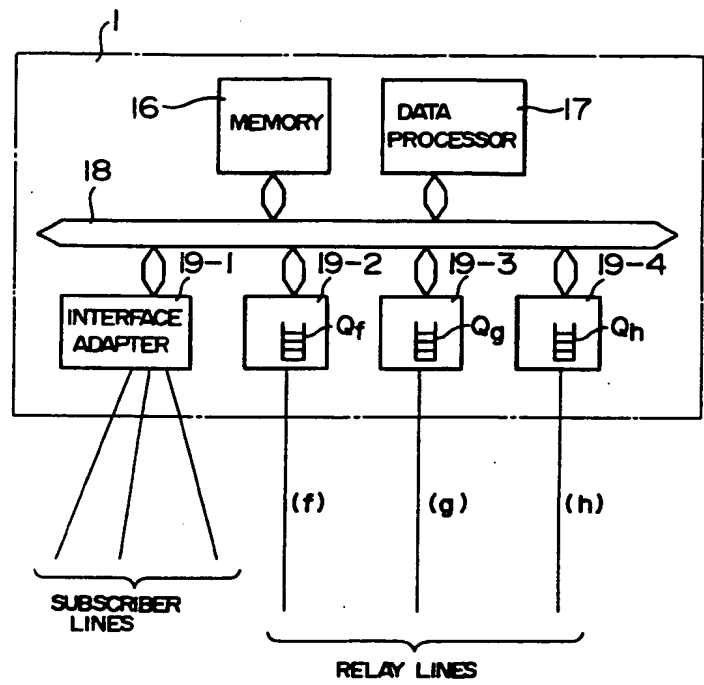


FIG. 3

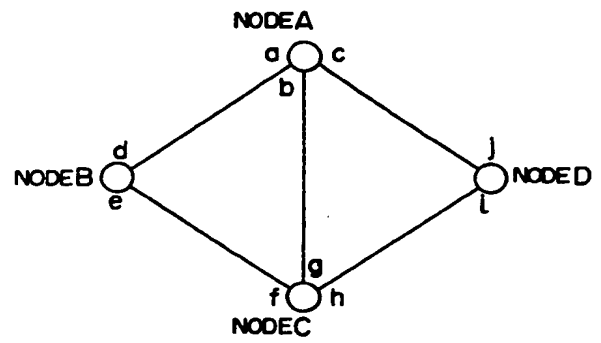


FIG. 4

	A	B	C	D
A		a	b	c
B	d			e
C	g	f		h
D	j		l	

20

FIG. 5

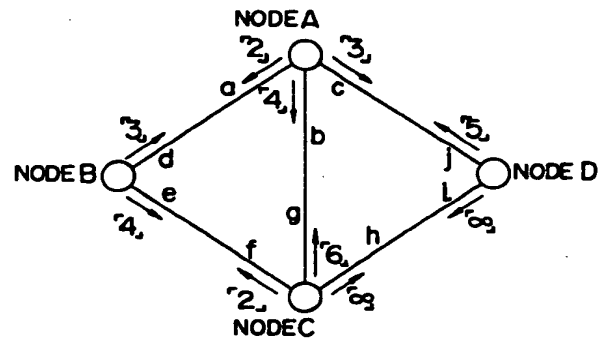


FIG. 6A

30A

a	2
b	4
c	3

FIG. 6B

30B

d	3
e	4

FIG. 6C

30C

f	2
g	6
h	∞

FIG. 6D

30D

i	∞
j	5

FIG. 7

SOURCE NODE	A			B		C			D	
	a	b	c	d	e	f	g	h	i	j
A				0	5	3	0	5	5	0
B	0	2	7			0	2	7	2	2
C	4	0	9	4	0				0	4
D	6	8	0	3	8	6	3	0		

21

31

FIG. 8

DESTINATION NODE	31		
	A	B	D
f	3	0	6
g	0	2	3
h	5	7	0

LINE

LINE	32	
	QUEUE	LENGTH
f	2	
g	4	
h	∞	

DESTINATION NODE	33		
	A	B	D
f	5	2	8
g	4	6	7
h	∞	∞	∞

SENDING
LINE

34		
g	f	g

F I G. 9

DESTINATION NODE

SOURCE NODE

	A	B	C	D
A		1	1	1
B	1		1	0
C	1	1		1
D	1	0	1	

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F I G. 10

DESTINATION NODE

SOURCE NODE

	A	C	D
d	0	1	1
e	1	0	1

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